

REMARKS

Claims 1-5 and 7-10 are pending in this application and have been rejected.

Claims 1 and 8 have been amended to incorporate the subject matter of claim 6, and so claim 6 has been cancelled. Claims 1 and 8 are independent.

Claims 1-10 have been rejected under 35 U.S.C. § 102(e)¹ as being anticipated by U.S. Patent No. 5,610,635 to Murray et al. Applicants respectfully traverse this rejection and submit the following arguments in support.

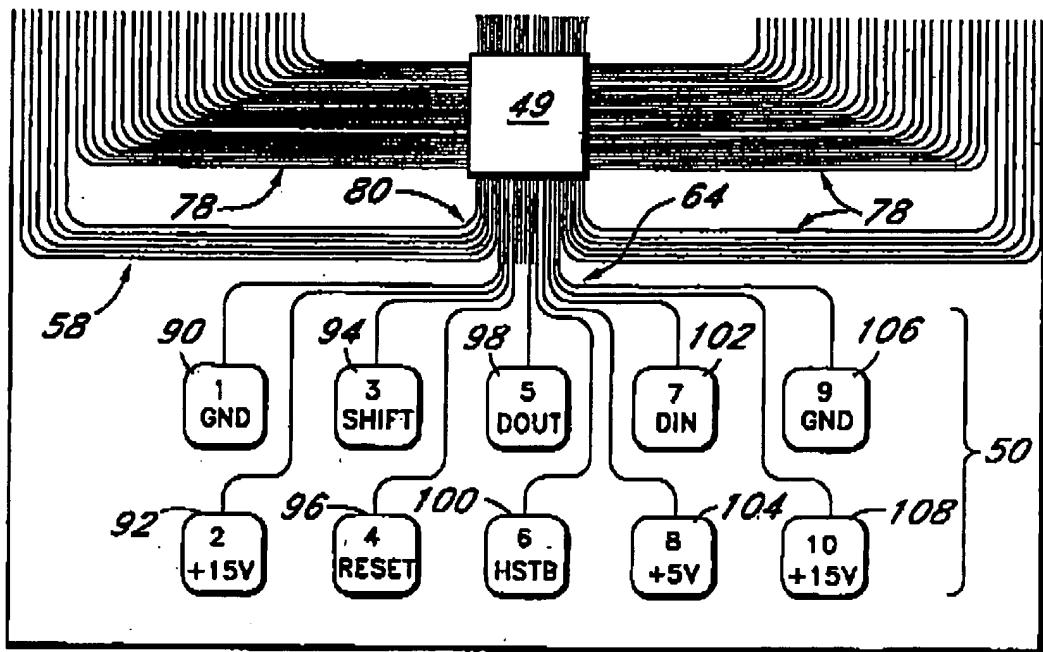
Applicants' invention, as set out in claim 1, involves a circuit board having a storage device for storing data relating to a marking material for printing. The circuit board includes at least two ground terminals arranged at two edges of the circuit board that are located on one axis, and plural terminals arranged on the circuit board for read/write operations on the data relating to the marking material for printing. The terminals include a power supply terminal and a control signal terminal. At least two ground terminals are not the terminals in closest proximity to the power supply terminal.

Claim 8 describes a circuit board with a storage device for storing data relating to a marking material for printing, the board being provided to a marking material for a print cartridge having a substantially rectangular shape. The circuit board has two ground terminals arranged on the circuit board at two edges thereof along one side, and terminals arranged parallel to one side of the circuit board, and used for storing the data. The terminals include a power supply terminal and a control signal terminal. The ground terminals are not the terminals in closest proximity to the power supply terminal.

¹ It is noted Murray is also available under other provisions of § 102.

The Office Action asserts that aspects of the claimed invention are taught by Fig. 6 of Murray. That, however, is not correct. Murray's terminals, as shown in Fig. 6, do not even suggest the claimed terminal arrangements.

Fig. 6 of Murray shows the arrangement below, and the location of ground terminals 1 and 9 and the power terminals 92, 104 and 108 should be noted (Murray refers at col. 7, lines 19-24, to the first +15 V power signal 92, a +5 V power signal 104, and a second +15 V power signal 108):



As explained at col. 7, lines 19-24, the +15 V power signals 92 and 108 are connected to the control and driver circuit, not the memory device 48. Those skilled in the art will appreciate that this voltage level is suitable for driving printhead ejection elements, not a memory device. Thus, these signals are not the same as or suggestive of the claimed power supply terminal, which supplies power to the memory device.

Those skilled in the art also would understand that only the +5V power signal 104 is appropriate for driving a memory device, and so only power signal 104 could correspond to the claimed power supply terminal. That is, Murray teaches just one power signal of the type recited in the claims.

Therefore, Murray cannot suggest the claimed invention, which provides for two power supply terminals, arranged so that the two ground terminals (or at least two ground terminals) are not the terminals in closest proximity to the power supply terminal (for supplying power for reading from and writing to the storage device).

In the event Murray's +15 V power signals 92 and 108 are treated as being power terminals, then it is clear from Fig. 6 that the ground signals 90 and 106 are the closest terminals to the power signals 92 and 108 (no signal is closer to power signals 92 and 108 than the grounds 90 and 106, respectively), and that in no ways suggests the ground terminals not be the terminals closest to the power supply terminal (in other words, the claims provide there is another terminal closer to the power supply terminal than the ground terminals).

For all the foregoing reasons, favorable reconsideration and withdrawal of this rejection are respectfully requested.

CONCLUSION

Applicants respectfully submit that all outstanding rejections have been addressed and are now either overcome or moot. Applicants further submit that all claims pending in this application are patentable over the prior art. Favorable reconsideration and withdrawal of those rejections and objections is respectfully requested.

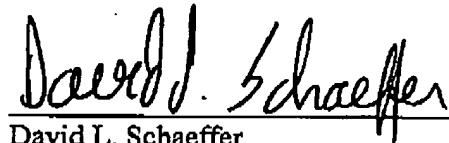
**Appln. No. 10/634,428
Amendment filed April 25, 2005
Reply to Office Action dated March 15, 2005**

No fees are believed to be due in connection with the filing of this Amendment.

Nevertheless, should the Commissioner deem any fee(s) to be now or hereafter due, the Commissioner is authorized to charge all such fees due in this application to Deposit Account No. 19-4709.

In the event that there are any questions, or should additional information be required, please contact Applicant's attorney at the number listed below.

Respectfully submitted,



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